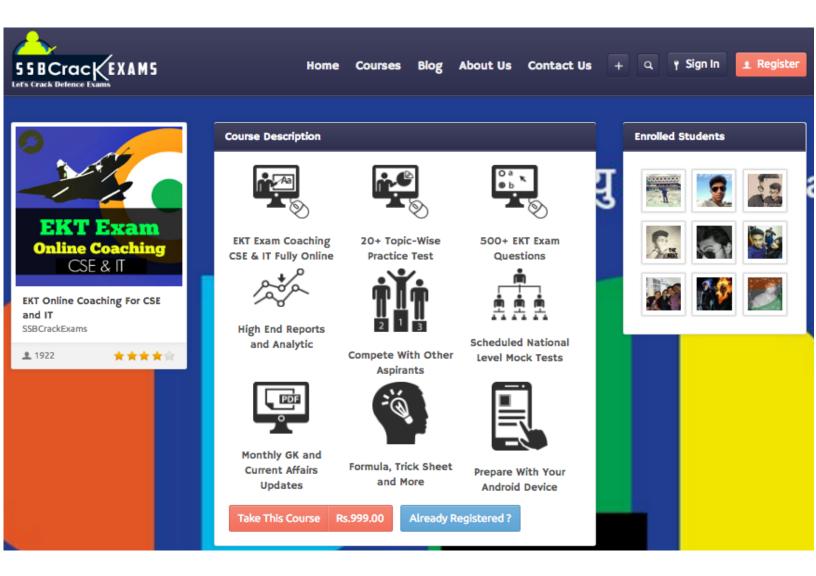
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EKT 2 2016 Question Paper CSE/IT









BOOKI FT SERIES 'P

TIME ALL OTTED: 45 MINUTES

		NS FOR CA	NDIDATE	S		TIME ALLO				
1.	Total r	number of C	uestions	50. Each C		of three ma	rks.			
2. 3.	One mark will be deducted for every wrong answer.									
Q1.		No marks will be deducted for un-attempted questions. Subtract (1010) ₂ from (1101) ₂ using 1st complement.								
Q1.	(a)	(1100) 2		(0011) 2	(c)	(1001) 2	(d)	(0101)2		
Q2.	Which (a) (c)	of the follow Static RAM EPROM		ories must b	e refreshed (b) (d)	many times Dynamic RA ROM		nd?		
Q3.	are op	ened togetl	her. The	cistern will I	be filled in	just half an	hour, if	the B is turne	pipes ed off	
	(a)	5 min.	(b)	9 min.	(c)	10 min.	(d)	15 min.		
Q4.	(a) (c)	strictly for i	al		(b)	strictly for o	ove			
Q5.	(a)	Router	(b)	Bridge	(c)	er LAN via th Repeater	e device (d)	Modem		
Q6.	What a	1. Sta	ndard ended (b)	4. Sp 2 and 4	EE pecialized (c)	3 and 4	(d)	1 and 2		
Q7.	Ether LAN uses (a) Polar encoding (c) Manchester encoding				(b)	Differentia NRZ	Differential manchester encoding NRZ			
Q8.	(2)	1/6	(b)	5/12	(c)	score is a pi	(0)			
Q9.	Four b compu	iter network	. What is	ked sequen the maximu 8	IIII MILIOOM	ring in a slidi size? 15	ng windo (d)	ow protocol us	ed in a	
Q10.	Which (a)	of the follow	wing tech (b)	nology can CMOS	give high (c)	speed RAM? ECL	(d)	NMOS		
Q11.	Which memory is difficult to interface with (a) Static memory (c) ROM				(d)	Dynamic memory None of these				
Q12.	(a) (c)	10111011	2	er,187 to 8-	bit binary. (b) (d)	1101110 1011110				
Q13.	A com (a) (b) (c) (d)	Program f Algorithms Discrete lo	lowchart s written	in computer	's languag	je			SET NO.	

Q14.	(a) Software interrupts (b) Interrupt and (c) Polled I/O (d) Direct memory access (DMA)							
Q15.	From a group of 7 men and 6 women, five persons are to be selected to form a committee so that at least 3 men are there on the committee. In how many ways can it be done? (a) 564 (b) 645 (c) 735 (d) 756							
Q16.	Description is an example of a(n)							
Q17.	Which of the following is the 1's complement of 10? (a) 01 (b) 110 (c) 11 (d) 10							
Q18.	Your router has the following IP address on Ethernet0: 172.16.2.1/23. Which of the following can be valid host IDs on the LAN interface attached to the router? 1. 172.16.1.100 2. 172.16.1.198 3. 172.16.2.255 (b) 2 and 3 only (c) 3 and 4 only (d) None of the above							
Q19.	The sum of ages of 5 children born at the intervals of 3 years each is 50 years. What is the age of the youngest child? (a) 4 years (b) 8 years (c) 10 years (d) None of these							
Q20.	The time required for the fetching and execution of one simple machine instruction is (a) Delay time (b) CPU cycle (c) Real time (d) Seek time							
Q21.	Which protocol reduces administrative overhead in a switched network by allowing the configuration of a new VLAN to be distributed to all the switches in a domain? (a) STP (b) VTP (c) DHCP (d) ISL							
Q22.	A voltage will influence current only if the circuit is (a) open (b) insulated (c) high resistance (d) closed							
Q23.	The full adder CKT adds Digit at a time (a) 1 (b) 2 (c) 3 (d) 4 (a) 1 (a) 1 (b) 2 (c) 3 (d) 4							
Q24.	Which of the following statements are true regarding the command ip route 172.16.4.0 255.255.255.0 192.168.4.2? 1. The command is used to establish a static route. 2. The default administrative distance is used. 3. The command is used to configure the default route. 4. The subnet mask for the source address is 255.255.255.0. 4. The subnet mask for the source address is 255.255.255.0. (a) 1 and 2 (b) 2 and 4 (c) 3 and 4 (d) All of the above							
Q25.	Process is: (a) contents of main memory (b) a job in secondary memory (c) a program in execution (d) in this level language kept on disk							
226.	(d) program in High level language rept A combinational logic circuit which is used when it is desired to send data from two or more sources through a single transmission line is known as (a) Encoder (b) Decoder (c) Multiplexer (d) Demultiplexer							









SET

Q39	(a)	ing of extra terre High transmittin Fully steerable	ng power			Very sensitive re All of the above			
Q40	(a) (b) (c) (d)	is much slower than the serial transmission of data. requires only one signal line between sender and receiver. requires as many signal lines between sender and receiver as there are data bits. is less expensive than the serial method of data transmission.							
Q41.	(a) (c)	exadecimal num 80 100	ber 'A0' has	s the decin		e equivalent to 256 160			
Q42	A wris (a) (c)	t grounding stra Surge protecto Voltmeter		which of th	e followi (b) (d)	ing: Capacitor Resistor			
Q43.	The re (a) (c)	egister which cor Index register Memory addre	Bertherley	struction t	(D)	Instruction reg	ISIGI	as	
Q44.	(a) (b) (c) (d)	A (Carrier Sense A method of cany time A method acce A very commo Network acces	ess control n bit-orientes s standard	technique ed data lin	vice has for mult k protoc cting sta	s access to the ciple-access tra col issued by IS ations to a circu	nsmissio SO. uit-switch	on media. ned network	
Q45.	What i	flavour of Networks users to conne	(b) Sta	obal Interr	(c)	Dynamic	(d)	PAT	
Q46.	called (a) (c)	hod for updating			(b) (d)	write-back cache-write	emoved	from the Ca	che is
247.	(a) (c)	M contents can Ultraviolet rays Burst of microv	vaves.		(d)	Intense hea	t radiation	ons.	
48.	(a) (c)	disk is divided i Clusters Vectors			(d)	Heads	ito:		
49.	(a)	outers, subtract 9's complement 1's complement	it	erally carr	(b) (d)	10's comple 2's comple			
	A JFET (a) i (c) i	s a current-cons a voltage-co	ntrolled de ntrolled de	vice	(b) (d)	has a low is always	input res forward-	sistance biased	
	A STATE OF								SET

